

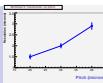
Radiation Tolerance of CMOS Sensors for the HFT :

Which Pixel Pitch for ULTIMATE ?

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OUTLINE

- Which ULTIMATE pitch is best suited to HFT non-ionising radiation ?
 - ※ what is the radiation dose ?
 - ※ safety margins ?
- What consequences on other critical parameters ?
 - ※ power dissipation
 - ※ pixel matrix read-out speed
 - ※ data throughput of output memories
- Where is the optimum ?
- Summary



■ High density of sampling nodes *allows faster collection of signal charge*

\hookrightarrow *prevents thermal diffusion of minority carriers \Rightarrow alleviates recombination probability (interstitial traps)*

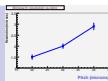
■ Benefits from pitch reduction :

 * *improved granularity \Rightarrow improved σ_{sp} \Rightarrow target value may be reached with less bits ... \rightarrow binary read-out*
 \hookrightarrow *digital-to-analog conversion becomes less power consuming*

■ Draw-backs from pitch reduction :

 * *nb of pixels / column \nearrow \Rightarrow $t_{int.} \nearrow \sim \text{pitch}^{-1}$*
 * *nb of columns / chip \nearrow \Rightarrow $P_{diss} \nearrow \sim \text{pitch}^{-0.8}$*
 * *total nb of pixels \nearrow \Rightarrow raw nb of noisy pixels $\nearrow \Rightarrow$ data throughput capabilities may saturate*

 \Rightarrow Optimal pixel pitch of ULTIMATE should account simultaneously for all these parameters



Non-Ionising Radiation Tolerance: Pitch Dependence

■ MIMOSA-15 ($20\text{ }\mu\text{m}$ pitch, analog output) irradiated with $\text{O}(1\text{ MeV})$ neutrons → tests at DESY (few GeV e^-)

- $T = -20^\circ\text{C}$, $t_{r.o.} \sim 700\text{ }\mu\text{s}$

- $5.8 \cdot 10^{12}\text{ n}_{eq}/\text{cm}^2$ values derived with **standard** and with **soft** cuts

| Fluence | 0 | 0.47 | 2.1 | 5.8 (5/2) | 5.8 (4/2) |
|---------------|----------------|----------------|----------------|--------------|--------------|
| S/N (MPV) | 27.8 ± 0.5 | 21.8 ± 0.5 | 14.7 ± 0.3 | $8.7 \pm 2.$ | $7.5 \pm 2.$ |
| Det. Eff. (%) | 100. | 99.9 ± 0.1 | 99.3 ± 0.2 | $77. \pm 2$ | $84. \pm 2$ |

■ MIMOSA-18 ($10\text{ }\mu\text{m}$ pitch, analog output) irradiated with $\lesssim 10^{13}\text{ O}(1\text{ MeV})\text{ n/cm}^2$ (+ 100–200 kRad γ gas)

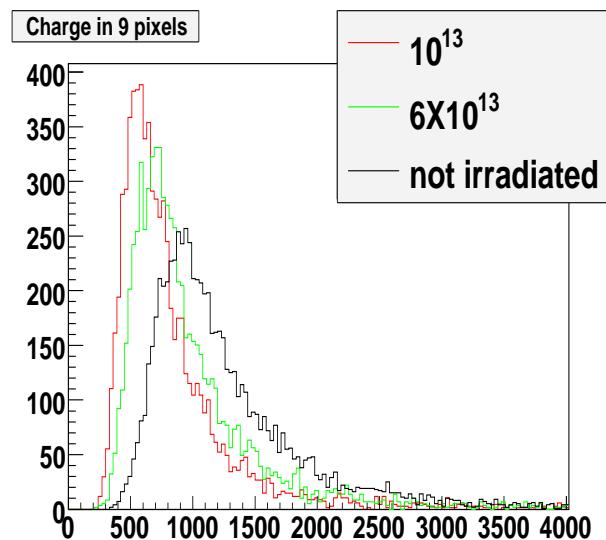
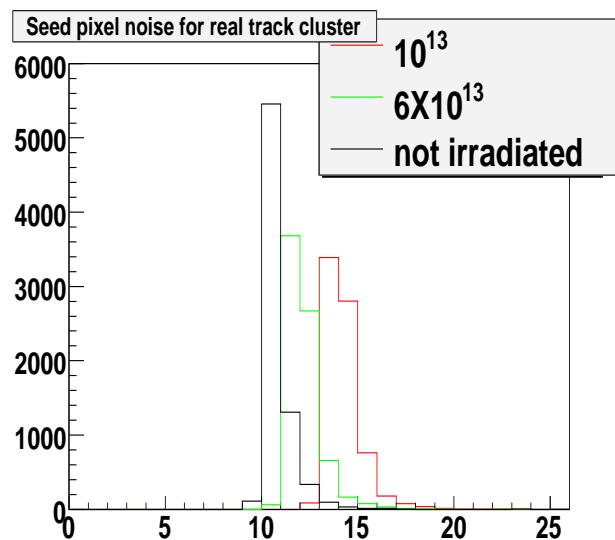
→ tested with $\sim 120\text{ GeV } \pi^-$ beam (CERN-SPS) in Summer-Autumn 2007

▷ Results obtained with :

- $T = -20^\circ\text{C}$

- $t_{r.o.} \sim 3\text{ ms}$

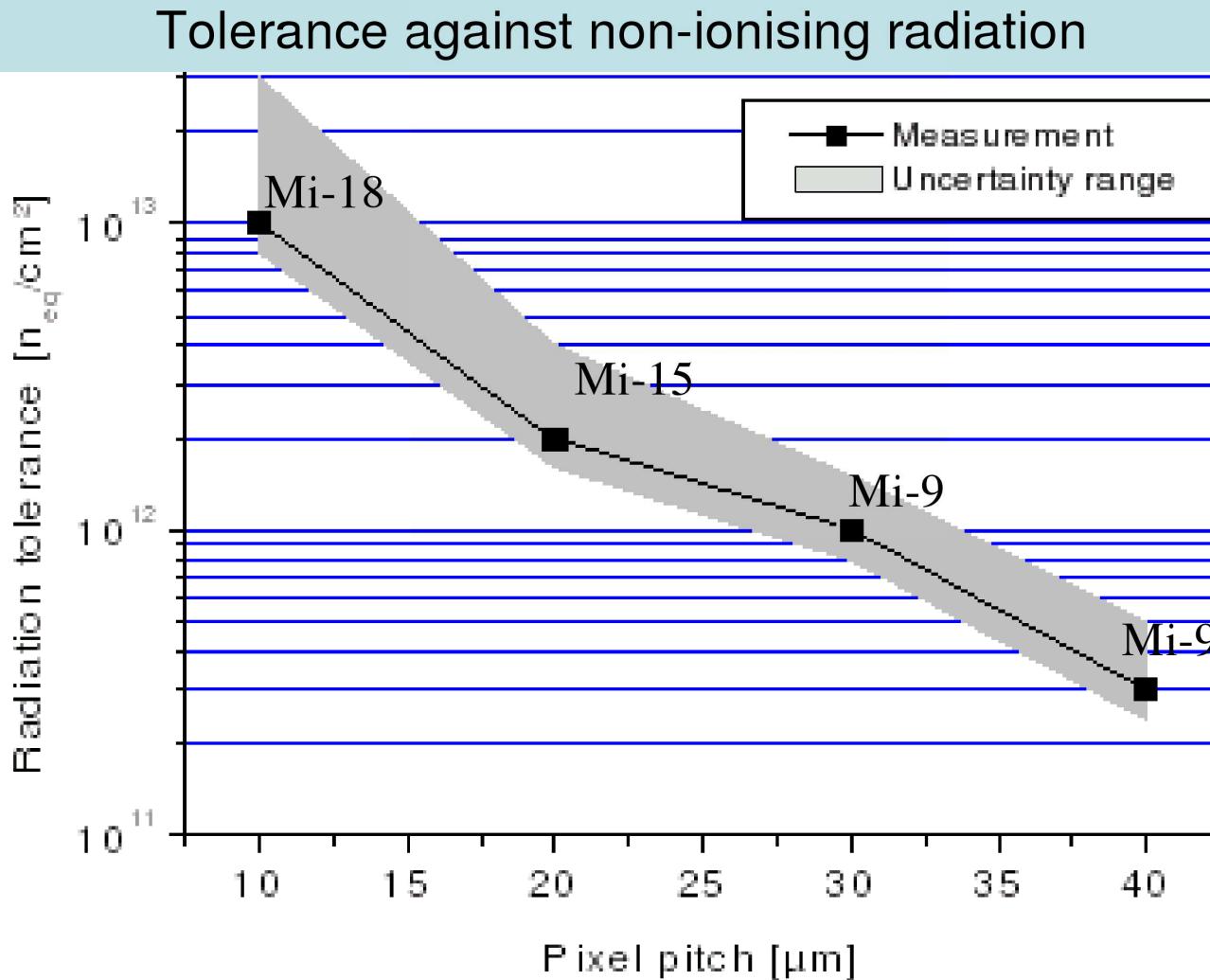
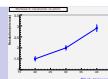
- cuts at 5N (seed) & 2N (crown)



| Fluence (n_{eq}/cm^2) | 0 | $6 \cdot 10^{12}$ | $1 \cdot 10^{13}$ |
|--|------------------|-------------------|-------------------|
| Noise ($e^- ENC$) ($-20^\circ\text{C}, 3\text{ ms}, 5\text{N}/2\text{N}$) | 10.8 ± 0.3 | 12.2 ± 0.3 | 14.3 ± 0.3 |
| $Q_{clust} (e^-)$ | 1026 | 680 | 560 |
| S/N (MPV) | 28.5 ± 0.2 | 20.4 ± 0.2 | 14.7 ± 0.2 |
| Det. Eff. (%) | 99.93 ± 0.03 | 99.85 ± 0.05 | 99.5 ± 0.1 |

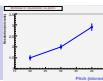
■ $\sim 10^{12}$ to $10^{13}\text{ n}_{eq}/\text{cm}^2/\text{s}$ affordable at $T < 0^\circ\text{C}$ & $t_{r.o.} \sim \text{O}(1)\text{ ms}$ & adapted pitch

→ study tolerance for digital output (larger pixel noise), $T > 0^\circ\text{C}$, , annealing ????



Main conclusion:

Small pixels seem mandatory to improve radiation hardness



■ "Optimum" preserving radiation tolerance, power dissipation, read-out speed, data flow (noise) ?

■ Assumed fluences to accommodate :

※ Inner layer : $\gtrsim 10^{12} n_{eq}/cm^2 \Rightarrow$ critical ※ Outer layer : $\gtrsim 10^{11} n_{eq}/cm^2 \Rightarrow$ not critical

■ Typical pitch values adapted to these very different fluences:

※ Inner layer : $\lesssim 20 \mu m$ ※ Outer layer : $\gtrsim 30 \mu m$

■ Consequences :

※ Power dissipation : $\sim 115 \text{ mW/cm}^2$ (outer layer) & $\sim 170 \text{ mW/cm}^2$ (inner layer) \Rightarrow average $\sim 130 \text{ mW/cm}^2$
 (\hookrightarrow equivalent to $25 \mu m$ pitch everywhere)

※ Read-out (integration) time : $\sim 130 \mu s$ (outer layer) & $\sim 200 \mu s$ (inner layer)

※ Data flow : see talk of Christine

■ Questions :

※ Are different integration times for both layers acceptable ?

※ Is a total 130 mW/cm^2 power dissipation (i.e. 10 % increase w.r.t. previous value) OK ?

※ Less conventional approach : rectangular pixels in outer layer
 Example : $18.4 \times 36.8 \mu m^2$ (outer) & $18.4 \times 18.4 \mu m^2$ (inner) \Rightarrow average $P_{diss} \sim 110 \text{ mW/cm}^2$